

PBPE-12P4

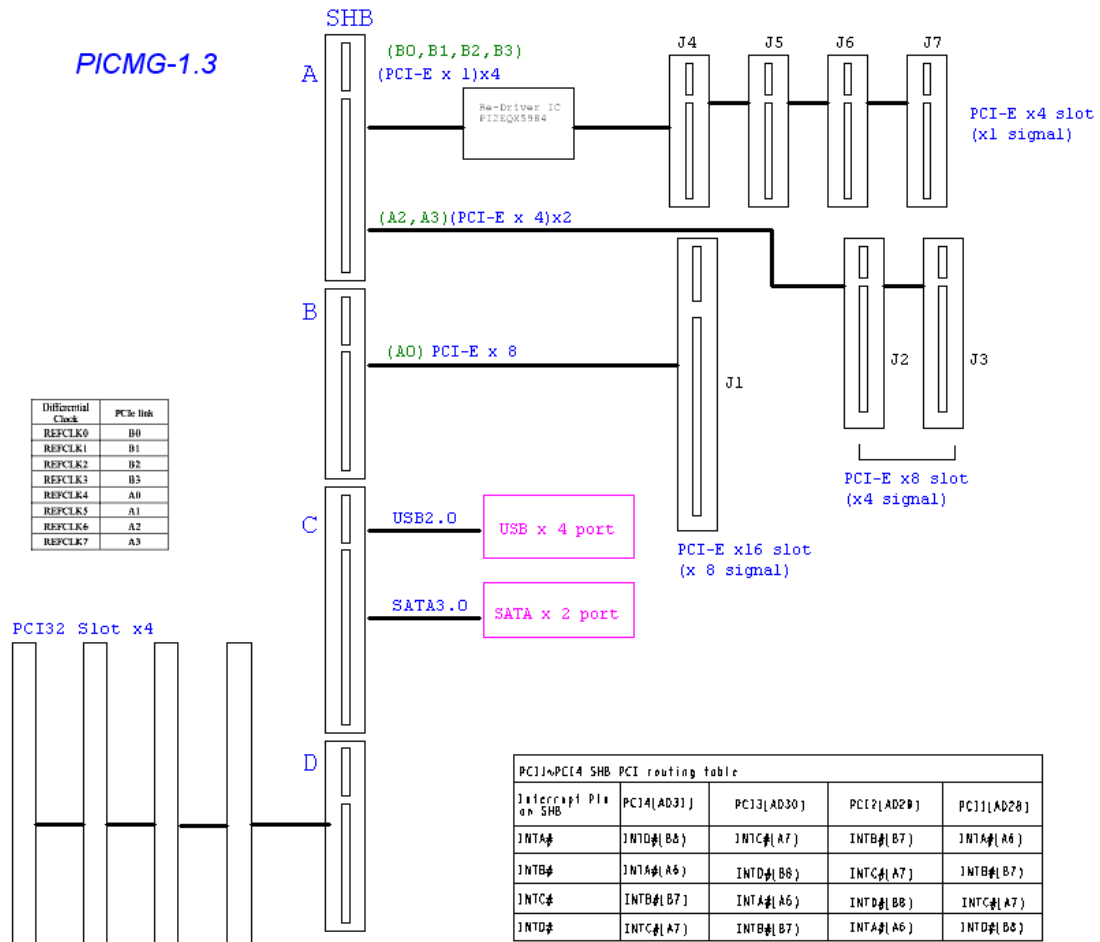
Backplane for PICMG1.3 SBC

Jumper setting

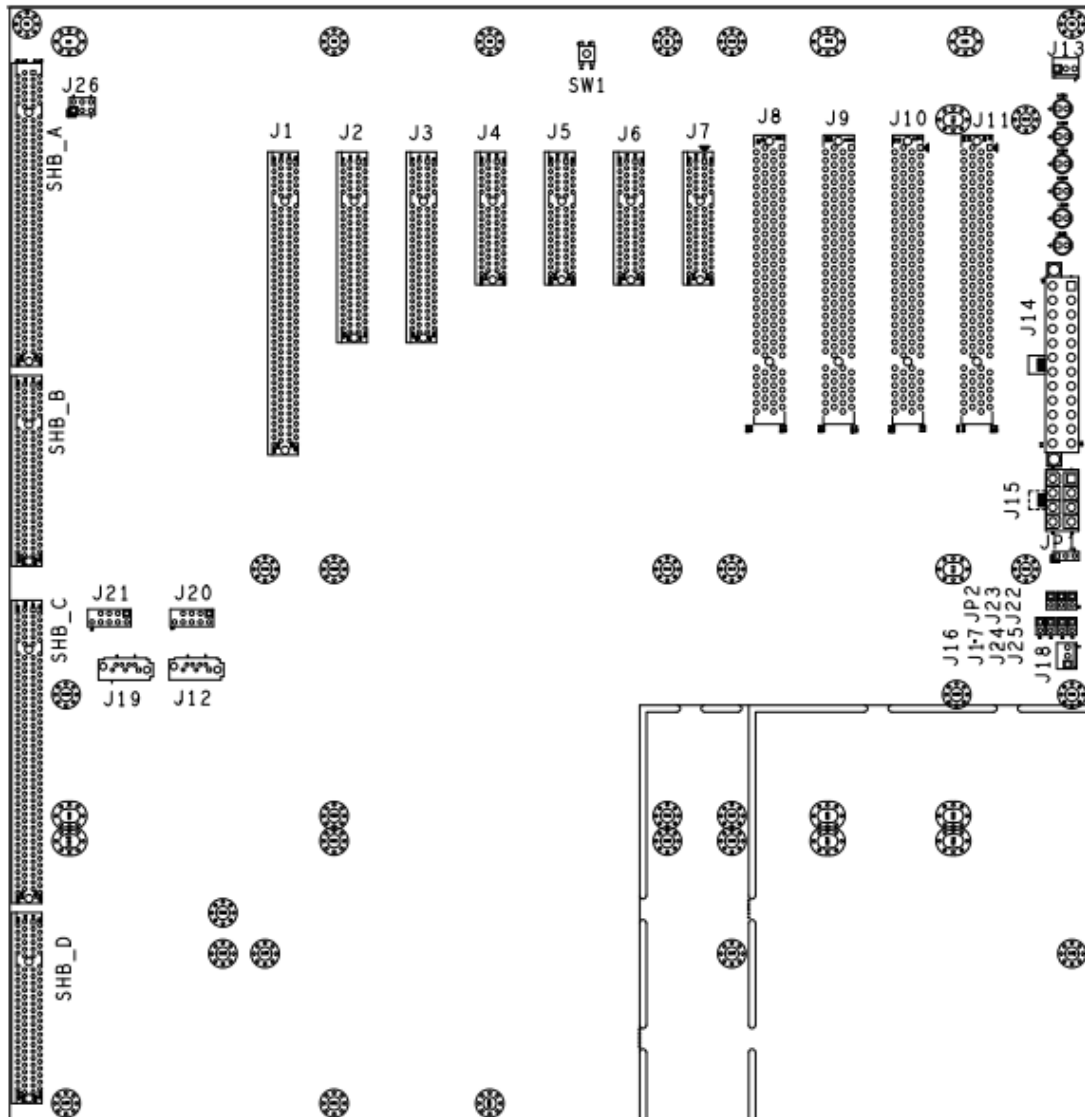
# Hardware Configuration

This chapter provides the definitions, the positions of jumpers and connector.

## Block Diagram



## Connector Location



## Connector's Function List

Connector Location	Description
<b>J1</b>	PCIe x16 Slot ,x8 signal
<b>J2,J3</b>	PCIe x8 Slot,x4 signal
<b>J4~J7</b>	PCIe x4 Slot,x1 signal
<b>J8~J11</b>	PCI slot
<b>J12,J19</b>	SATA 3.0 Connector
<b>J13,J18</b>	System Fan Connector
<b>J14</b>	ATX power 24 pin Connector
<b>J15</b>	+12V ATX power 8 pin Connector
<b>J16</b>	System Power LED from SBC pin header
<b>J17</b>	SATA disk LED from SBC pin header

<b>J20,J21</b>	USB2.0 x2 Pin Header
<b>J22</b>	System Reset Pin Header
<b>J23</b>	ATX Power –On Pin Header
<b>J24</b>	System Power LED to Panel pin header
<b>J25</b>	SATA disk LED to Panel pin header
<b>J26</b>	Micro-controller SMBus pin header (for test only)
<b>SHB_A</b>	PICMG 1.3 slot A
<b>SHB_B</b>	PICMG 1.3 slot B
<b>SHB_C</b>	PICMG 1.3 slot C
<b>SHB_D</b>	PICMG 1.3 slot D

### Pin Assignments of Connectors

#### J13,J18 :System Fan Connector

<b>PIN No.</b>	<b>Signal Description</b>
1	Ground
2	+12V
3	N/C

#### J15 :+12V ATX Power 8 pin connector

<b>PIN No.</b>	<b>Signal Description</b>	<b>PIN No.</b>	<b>Signal Description</b>
1	GND	5	+12V
2	GND	6	+12V
3	GND	7	+12V
4	GND	8	+12V

#### J22: System Reset pin header

<b>PIN No</b>	<b>Signal Description</b>
1	Reset Signal Input (Active low)
2	Ground

**J23: ATX Power-On pin header**

PIN No.	Signal Description
1	Pull-high 1K Ohm to +5VSB
2	Power Button Signal Input (Active high)

**J16: System Power LED pin header( from SBC)**

PIN No.	Signal Description
1	power LED+
2	power LED-

**J17: SATA Disk LED pin header(from SBC)**

PIN No.	Signal Description
1	SATA LED+
2	SATA LED-

**J24: System Power LED pin header( to Panel)**

PIN No.	Signal Description
1	power LED+
2	power LED-

**J25: SATA Disk LED pin header(to Panel)**

PIN No.	Signal Description
1	SATA LED+
2	SATA LED-

**J20,J21: USB2.0 x2 pin header**

PIN No.	Signal Description	PIN No.	Signal Description
1	+5V	2	+5V
3	USB1 D-	4	USB2 D-
5	USB1 D+	6	USB2 D+
7	Ground	8	Ground
9	N/C	10	Earths GND

**JP1: PCI\_VIO Voltage selection jumper**

<b>PIN No.</b>	<b>Signal Description</b>
1-2	voltage is provided by a +3V power
2-3	voltage is provided by a +5V power (default)

**SW2:**

<b>PIN No. 1-4</b>	<b>Signal Description</b>
On	Function enabled
Off	Function disabled (default)